

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L29	2	jui same lung same hung and ching same tung same wang and (LCD or liquid adj crystal adj display) and low same color same scale	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 09:32
L30	2	jui same lung same hung and (LCD or liquid adj crystal adj display) and low same color same scale	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 09:32

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		driving adj circuit and liquid adj crystal adj display and device and including and timing and controller and source and driver and low adj color adj scale and driving and circuit and timing and controller and low and color and scale and driving and circuit and generates and analog signal and buffer and receiving and polarity same inverting and signal and digital and signal and outputted and timing and controller and transistors and coupled and output	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:58
L1	1768	display and cmos and transisotr\$1 and aND POWER AND DRAIN AND SOURCE	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 13:48
L2	41	display and four adj set\$6 adj transistor\$1 and (LCD or liquid adj crystal adj display) and driv\$6 adj circuit\$6 and (low\$6 and color\$6 adj scale\$6) and generat\$6 and analog adj signal\$6 and (polarity\$6 adj invert\$6 and response and generat\$6 and digital adj signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 13:57
		driving and circuit and liquid and crystal and display and device and including and timing and controller and source and driver and low and color and scale and driving	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:16
		driving and circuit and liquid and crystal and display and device and including and timing and controller and source and driver and low and color and scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:16

EAST Search History

L3	15	driving and circuit and liquid and crystal and display and device and including and timing and controller and source and driver and low and color and scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:16
L4	14	driving and circuit and liquid and crystal and display and device and including and timing and controller and source and driver and low and color and scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving and "345"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:18
L5	15	driving and circuit and liquid and crystal and display and device and including and timing and controller and source and driver and low and color and scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:20

EAST Search History

L6	15	driving and circuit and liquid and crystal and display and device and including and timing and controller and source and driver and low and color and scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and plurality and sets and transistors and comprise and first	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:25
L7	8	driving and circuit and liquid and crystal and display and device and including and timing and controller and source and driver and low and color and scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and plurality and sets and transistors and comprise and first	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:25

EAST Search History

L8	0	driving and circuit and liquid and crystal and display and device and including and timing and controller and source and driver and low and color and scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and plurality and sets and transistors and comprise and first and set and transistors and second and set and transistors and third and set and transistors and fourth and set and transistors and respectively and coupled and output and terminal and buffers and respectively and outputting and first and second and third and fourth and analog and signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:25
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EAST Search History

L9	8	driving and circuit and liquid and crystal and display and device and including and timing and controller and source and driver and low and color and scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and plurality and sets and transistors and comprise and first	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:26
L10	0	driving and circuit and liquid and crystal and display and device and including and timing and controller and source and driver and low and color and scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and plurality and sets and transistors and comprise and first and set and transistors and second and set and transistors and third and set and transistors and fourth and set and transistors and respectively and coupled and output	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:26

EAST Search History

L11	8	driving and circuit and liquid and crystal and display and device and including and timing and controller and source and driver and low and color and scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and plurality and sets and transistors and comprise and first and set and transistors and second and set and transistors and third and set and transistors and fourth and set	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:26
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EAST Search History

L12	0	driving and circuit and liquid and crystal and display and device and including and timing and controller and source and driver and low and color and scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and plurality and sets and transistors and comprise and first and set and transistors and second and set and transistors and third and set and transistors and fourth and set and transistors and (respectively or (in same order)) and coupled and output and terminal and buffers and respectively and outputting and first and second and third and fourth and analog and signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:27
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EAST Search History

L13	0	driving and circuit and liquid and crystal and display and device and including and timing and controller and source and driver and low and color and scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and plurality and sets and transistors and comprise and first and set and transistors and second and set and transistors and third and set and transistors and fourth and set and transistors and coupled and output and terminal and buffers and respectively and outputting and first and second and third and fourth and analog and signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:27
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EAST Search History

L14	6	driving and circuit and liquid and crystal and display and device and including and timing and controller and source and driver and low and color and scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and plurality and sets and transistors and comprise and first and set and transistors and second and set and transistors and third and set and transistors and fourth and set and transistors and coupled and output and terminal and buffers and (respectively or (in same order)) and outputting and first and second and third and fourth and analog and signal and (respectively or (in same order))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:29
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EAST Search History

L15	6	driving and circuit and liquid adj crystal adj display and device and including and timing and controller and source and driver and low and color and scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and plurality and sets and transistors and comprise and first and set and transistors and second and set and transistors and third and set and transistors and fourth and set and transistors and coupled and output and terminal and buffers and (respectively or (in same order)) and outputting and first and second and third and fourth and analog and signal and (respectively or (in same order))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:30
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EAST Search History

L16	3	driving adj circuit and liquid adj crystal adj display and device and including and timing and controller and source and driver and low and color and scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and plurality and sets and transistors and comprise and first and set and transistors and second and set and transistors and third and set and transistors and fourth and set and transistors and coupled and output and terminal and buffers and (respectively or (in same order)) and outputting and first and second and third and fourth and analog and signal and (respectively or (in same order))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:30
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EAST Search History

L17	1	driving adj circuit and liquid adj crystal adj display and device and including and timing and controller and source and driver and low adj color adj scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog and signal and plurality and buffers and receiving and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and plurality and sets and transistors and comprise and first and set and transistors and second and set and transistors and third and set and transistors and fourth and set and transistors and coupled and output and terminal and buffers and (respectively or (in same order)) and outputting and first and second and third and fourth and analog and signal and (respectively or (in same order))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:50
L18	1	driving adj circuit and liquid adj crystal adj display and device and including and timing and controller and source and driver and low adj color adj scale and driving and circuit and timing and controller and outputting and polarity same inverting and signal and first and digital and signal and second and digital and signal and third and digital and signal and fourth and digital and signal and low and color and scale and driving and circuit and generating and first and analog and signal and second and analog and signal and third and analog and signal and fourth and analog	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:50

EAST Search History

L19	1	driving adj circuit and liquid adj crystal adj display and device and including and timing and controller and source and driver and low adj color adj scale and driving and circuit and timing and controller and low and color and scale and driving and circuit and generates and analog and signal and buffer and receiving and polarity same inverting and signal and digital and signal and outputted and timing and controller and transistors and coupled and output	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 08:59
L20	0	driving adj circuit and liquid adj crystal adj display and device and including and timing and controller and source and driver and low adj color adj scale and driving and circuit and timing and controller and low and color and scale and driving and circuit and generates and analog and signal and buffer and receiving and polarity same inverting and signal and digital and signal and outputted and timing and controller and transistors and coupled and output and terminal and buffer and outputting and analog and signal and driving and circui and claim and set and transistors and PMOS and transistor and NMOS and transistor and gate and PMOS and transistor and gate and NMOS and transistor and coupled and output and terminal and buffer and source and PMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 09:00
L21	0	driving adj circuit and liquid adj crystal adj display and device and including and timing and controller and source and driver and low adj color adj scale and driving and circuit and timing and controller and low and color and scale and driving and circuit and generates and analog and signal and buffer and receiving and polarity same inverting and signal and digital and signal and outputted and timing and controller and transistors and coupled and output and terminal and buffer and outputting and analog and signal and driving and circui and claim and set and transistors and PMOS and transistor and NMOS and transistor and gate and PMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 09:00

EAST Search History

L22	0	driving adj circuit and liquid adj crystal adj display and device and including and timing and controller and source and driver and low adj color adj scale and driving and circuit and timing and controller and low and color and scale and driving and circuit and generates and analog and signal and buffer and receiving and polarity same inverting and signal and digital and signal and outputted and timing and controller and transistors and coupled and output and terminal and buffer and outputting and analog and signal and driving and circuit and claim and set and transistors and PMOS and transistor and NMOS and transistor and gate and PMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 09:00
L23	0	driving adj circuit and liquid adj crystal adj display and device and including and timing and controller and source and driver and low adj color adj scale and driving and circuit and timing and controller and low and color and scale and driving and circuit and generates and analog and signal and buffer and receiving and polarity same inverting and signal and digital and signal and outputted and timing and controller and transistors and coupled and output and terminal and buffer and outputting and analog and signal and driving and circuit and claim and set	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 09:01
L24	1	driving adj circuit and liquid adj crystal adj display and device and including and timing and controller and source and driver and low adj color adj scale and driving and circuit and timing and controller and low and color and scale and driving and circuit and generates and analog and signal and buffer and receiving and polarity same inverting and signal and digital and signal and outputted and timing and controller and transistors and coupled and output and terminal and buffer and outputting and analog and signal and driving and circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 09:01

EAST Search History

L25	1	driving adj circuit and liquid adj crystal adj display and device and including and timing and controller and source and driver and low adj color adj scale and driving and circuit and timing and controller and low and color and scale and driving and circuit and generates and analog and signal and buffer and receiving and polarity same inverting and signal and digital and signal and outputted and timing and controller and transistors and coupled and output and terminal and buffer and outputting and analog and signal and driving and circuit and transistors and PMOS and transistor and NMOS and transistor and gate and PMOS and transistor and gate and NMOS and transistor and coupled and output and terminal and buffer and source and PMOS and transistor and coupled and drain and NMOS and transistor and drain and PMOS and transistor and coupled and power and voltage and source and NMOS and transistor and coupled and ground and voltage and analog and signal and outputted and through and source and PMOS and transistor and drain and NMOS and transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 09:01
L26	24	ching same tung same wang	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 09:02
L27	12	ching same tung same wang and (LCD or liquid adj crystal adj display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 09:03
L28	2	ching same tung same wang and (LCD or liquid adj crystal adj display) and low same color same scale	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/25 09:03
S1	405607	(LCD or liquid adj crystal adj display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:21

EAST Search History

S2	39888	(LCD or liquid adj crystal adj display) and driv\$6 adj circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 13:52
S3	2	(LCD or liquid adj crystal adj display) and driv\$6 adj circuit\$6 and low adj color adj scale	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 09:03
S4	2	(LCD or liquid adj crystal adj display) and driv\$6 adj circuit\$6 and low\$6 adj color\$6 adj scale\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 13:53
S5	79	(LCD or liquid adj crystal adj display) and driv\$6 adj circuit\$6 and (low\$6 and color\$6 adj scale\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 13:54
S6	11	(LCD or liquid adj crystal adj display) and driv\$6 adj circuit\$6 and (low\$6 and color\$6 adj scale\$6) and generat\$6 and analog adj signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 14:03
S7	1	(LCD or liquid adj crystal adj display) and driv\$6 adj circuit\$6 and (low\$6 and color\$6 adj scale\$6) and generat\$6 and analog adj signal\$6 and (polarity\$6 adj invert\$6 and response and generat\$6 and digital adj signal\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 13:57
S8	27	(LCD or liquid adj crystal adj display) and driv\$6 adj circuit\$6 and (low\$6 and color\$6 and scale\$6) and generat\$6 and analog adj signal\$6 and (polarity\$6 adj invert\$6 and response and generat\$6 and digital adj signal\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 10:47
S9	405607	(LCD or liquid adj crystal adj display) low adj color adj scal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 14:29
S10	2	(LCD or liquid adj crystal adj display) and low\$6 adj color\$6 adj scal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 14:30

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S11	4	display and low\$6 adj color\$6 adj scal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 14:35
S12	1474	display and low\$6 and color\$6 adj scal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 14:33
S13	691	display and (low\$6 and upper) and color\$6 adj scal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 14:33
S14	701	display and (low\$6 and upper\$6) and color\$6 adj scal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 14:33
S15	372	display and (low\$6 and upper\$6) and color\$6 adj scal\$6 and generat\$6 and weight\$6 and value	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 14:48
S16	1	display and image and correction and produce and revised and image and plurality and pixels and color adj scale and values and calculating and frequency and value and color adj scale and threshold and limiting and summation and frequency	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 14:39
S17	13	display\$6 and image\$6 and correct\$6 and produce\$6 and revis\$6 and image\$6 and plurality and pixel\$1 and color adj scale and values and calculat\$6 and frequency and value and color\$6 adj scale\$6 and threshold and limit\$6 and sum\$6 and frequency	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:12
S18	0	display and (low\$6 and upper\$6) and (color\$6 adj scal\$6 and driv\$6 and circuit\$6) and generat\$6 and weight\$6 and value and analog adj signal\$6 and invert\$6 and polarity\$6 and geneart\$6 and (digital and signal\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 14:51
S19	5	display and (low\$6 and upper\$6) and (color\$6 adj scal\$6 and driv\$6 and circuit\$6) and generat\$6 and weight\$6 and value and analog adj signal\$6 and invert\$6 and polarity\$6 and generat\$6 and (digital and signal\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 14:52

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S20	4	display and low\$6 adj color\$6 adj scal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:10
S21	2	low\$6 adj color\$6 adj scal\$6 adj driv\$6 adj circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:11
S22	18	display\$6 and image\$6 and pixel\$1 and lower and color\$6 adj scale\$6 and (geneart\$6 and digital and signal and after polarit\$6 and invert\$6 and generat\$6 and analog and signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:15
S23	2	(LCD or liquid adj crystal adj display) and color adj scale adj driv\$6 adj circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:22
S24	71	(LCD or liquid adj crystal adj display) and color adj scale and driv\$6 adj circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:22
S25	79	(LCD or liquid adj crystal adj display) and (color\$6 adj scale\$6 and driv\$6 adj circuit\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:23
S27	9	(LCD or liquid adj crystal adj display) and (color\$6 adj scale\$6 and driv\$6 adj circuit\$6) and generat\$6 and digital adj signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 16:23
S28	0	(LCD or liquid adj crystal adj display) and (color\$6 adj scale\$6 and driv\$6 adj circuit\$6) and generat\$6 and digital adj signal\$6 and invert\$6 adj polarity\$6 and generat\$6 and analog adj signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:24
S29	0	(LCD or liquid adj crystal adj display) and (color\$6 adj scale\$6 and driv\$6 adj circuit\$6) and generat\$6 and digital adj signal\$6 and invert\$6 adj polarit\$6 and generat\$6 and analog adj signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 15:25

EAST Search History

S30	3	(LCD or liquid adj crystal adj display) and (color\$6 adj scale\$6 and driv\$6 adj circuit\$6) and generat\$6 and digital adj signal\$6 and invert\$6 and polarit\$6 and generat\$6 and analog adj signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 16:13
S31	7	(LCD or liquid adj crystal adj display) and (color\$6 adj scale\$6 and driv\$6 adj circuit\$6) and (generat\$6 and digital and signal\$6) and (invert\$6 and polarit\$6 and generat\$6 and analog and signal\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/11 16:14
S32	2	(LCD or liquid adj crystal adj display) and low adj color adj scale	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 10:54
S33	2	display and low adj color adj scale	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 10:55
S34	9	(LCD or liquid adj crystal adj display) and (low and color\$6 adj scale\$6 and driv\$6 adj circuit\$6) and generat\$6 and digital adj signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:13
S35	4	display and low\$6 adj color\$6 adj scale\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 09:16
S36	2	display and low\$6 adj color\$6 adj scale\$6 and (low and color\$6 adj scale\$6 and driv\$6 adj circuit\$6) and generat\$6 and digital adj signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 09:12
S37	2	display and low\$6 adj color\$6 adj scale\$6 and (driv\$6 adj circuit\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 09:13
S38	3	display and low\$6 adj color\$6 adj scale\$6 and (driv\$6 and circuit\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 09:13

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S39	2	display and low\$6 adj color\$6 adj scale\$6 and digital adj signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 09:14
S40	2	display and low\$6 adj color\$6 adj scale\$6 and digit\$6 and signal\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 09:15
S41	3	display and low\$6 adj color\$6 adj scale\$6 and digit\$6 and analog\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 09:16
S42	3	display and low\$6 adj color\$6 adj scale\$6 and analog\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 09:16
S43	2	"5,300,927".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 10:03
S44	2	"5,798,752".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 10:04
S45	2	"6,674,425".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 10:04
S46	2	"5,194,852".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 10:05
S47	2	"20030080940"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 10:07

EAST Search History

S48	15	(LCD or liquid adj crystal adj display) and driv\$6 adj circuit\$6 and (low\$6 and color\$6 and scale\$6) and generat\$6 and analog adj signal\$6 and (polarity\$6 adj invert\$6 and response and generat\$6 and digital adj signal\$6) and buffer\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 10:47
S49	3	(LCD or liquid adj crystal adj display) and driv\$6 adj circuit\$6 and (low\$6 and color\$6 and scale\$6) and generat\$6 and analog adj signal\$6 and (polarity\$6 adj invert\$6 and response and generat\$6 and digital adj signal\$6) and (buffer\$6 and receiv\$6 and polarity\$6 adj invert\$6 and generat\$6 and digital adj signal\$6) and (transistor\$6 and couple\$6 and output\$6 and analog adj signal\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 10:50
S50	3	(LCD or liquid adj crystal adj display) and driv\$6 adj circuit\$6 and (low\$6 and color\$6 and scale\$6) and generat\$6 and analog adj signal\$6 and (polarity\$6 adj invert\$6 and response and generat\$6 and digital adj signal\$6) and (buffer\$6 and receiv\$6 and polarity\$6 adj invert\$6 and generat\$6 and digital adj signal\$6) and (transistor\$6 and couple\$6 and output\$6 and generat\$6 and analog adj signal\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 10:51
S51	641	display and set adj transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 10:55
S52	405607	(LCD or liquid adj crystal adj display)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 10:55
S53	83	display and set adj transistor\$1 and PMOS and NMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 13:59
S54	18	display and set adj transistor\$1 and PMOS and NMOS and (gate adj PMOS and gate adj NMOS and couple\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 10:57

EAST Search History

S55	16	display and set adj transistor\$1 and PMOS and NMOS and (gate adj PMOS and gate adj NMOS and couple\$6 and output\$6 and buffer\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 11:14
S56	1	display and set adj transistor\$1 and PMOS and NMOS and (gate adj PMOS and gate adj NMOS and couple\$6 and output\$6 and buffer\$6) and (source adj PMOS and couple\$6 and drain adj NMOS)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 11:09
S57	7	display and four adj set\$6 adj transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 13:47
S58	1007	345/690.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 11:49
S59	2	345/690.ccls. and (low\$6 and color\$6 adj scale\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 11:51
S60	3	345/690.ccls. and (color\$6 adj scale\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 11:51
S61	0	"200402227713"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 12:10
S62	2	"200402227713"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 12:11
S63	1616	345/98.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 12:11

EAST Search History

S64	69	345/98.ccls. and analog adj digital	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 12:20
S65	6	345/98.ccls. and analog adj convert\$6 adj digital	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 12:42
S66	134	345/98.ccls. and (analog adj convert\$6 adj digital or A/D)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 12:43
S67	128	345/98.ccls. and A/D	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 12:43
S68	0	345/98.ccls. and A/D and color adj scale	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 12:43
S69	0	345/98.ccls. and A/D and color\$6 adj scale\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 12:44
S70	3	display and cmos and transisotr\$1 and POWER AND DRAIN AND SOURCE	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 13:49
S71	3	display and cmos and transisotr\$1 and POWER AND DRAIN AND SOURCE AND BUFFER	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 13:52
S72	3	display and cmos AND DRIVE\$6 and transisotr\$1 and POWER AND DRAIN AND SOURCE AND BUFFER	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 13:53
S73	22540	display and cmos AND DRIVE\$6 and transisotr\$1 and POWER PUSH\$6 AND PULL\$6 AND BUFFER	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 13:53

EAST Search History

S74	1	display and cmos AND DRIVE\$6 and transisotr\$1 and POWER AND PUSH\$6 AND PULL\$6 AND BUFFER	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 13:54
S75	77	display and set adj transistor\$1 and (PMOS and NMOS)AND CONNECT\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 13:59
S76	26	display and set adj transistor\$1 and (PMOS and NMOS)AND CONNECT\$6 and output\$6 adj buffer\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 13:59
S77	26	display and set adj transistor\$1 and (PMOS and NMOS) AND CONNECT\$6 and output\$6 adj buffer\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 14:01
S78	128	345/98.ccls. and A/D	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 14:00
S79	51775	display and set adj transistor\$1 and output\$6 adj buffer\$6 AND CONNECT\$6 amd (PMOS and NMOS)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 14:44
S80	27901	display and output\$6 adj buffer\$6 AND CONNECT\$6 amd (PMOS and NMOS) adj set adj transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 14:45
S81	51327	display and output\$6 adj buffer\$6 AND CONNECT\$6 amd (PMOS and NMOS) adj transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 14:46
S82	38055	display and control\$6 and polarity adj invert\$6 and output\$6 adj buffer\$6 AND CONNECT\$6 amd (PMOS and NMOS) adj transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 14:47
S83	6	display and control\$6 and polarity adj invert\$6 and output\$6 adj buffer\$6 AND CONNECT\$6 and (PMOS and NMOS) adj transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 14:47

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S84	5	(LCD or liquid adj crystal adj display) and (low and color\$6 adj scale\$6 and driv\$6 adj circuit\$6) and generat\$6 and digital adj signal\$6 and invert\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:21
S85	5	(LCD or liquid adj crystal adj display) and (low and color\$6 adj scale\$6 and driv\$6 adj circuit\$6) and generat\$6 and digital adj signal\$6 and invert\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:22
S86	2	"20020140711"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:22
S87	1	"20020140711" and digital	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:28
S88	1	"20020140711" and ANALOG	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 15:59
S89	8	(LCD or liquid adj crystal adj display) and (color\$6 adj scale\$6 and driv\$6 adj circuit\$6) and generat\$6 and digital adj signal\$6 and analog	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/12 16:24
S90	1	display and low adj color adj scale\$6 adj circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 13:54
S91	1	display and low adj color same scale\$6 adj circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 13:54
S92	1	display and low\$6 adj color\$6 same scale\$6 adj circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:10
S93	6	display and low\$6 adj color\$6 same scale\$6 same circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:00

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S94	395	display and low\$6 same color\$6 same scale\$6 same circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:01
S95	160	display and low\$6 same color\$6 same scale\$6 same circuit\$6 and "345"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:01
S96	16	display and low\$6 same color\$6 same scale\$6 same circuit\$6 and "345"/87.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:05
S97	7	display and low\$6 same color\$6 same scale\$6 same driv\$6 same circuit\$6 and "345"/87.cccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 16:39
S99	3	display and low\$6 adj color\$6 adj scale\$6 and circuit\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:11
S100	4	display and low\$6 adj color\$6 adj scale\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:11
S101	2	"20020140711"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:15
S102	2	"20020126106"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:15
S103	2	"20040131279"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:15
S104	9	(LCD or liquid adj crystal adj display) and (color\$6 adj scale\$6 and driv\$6 adj circuit\$6) and generat\$6 and digital adj signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:49

EAST Search History

S10 5	7	(LCD or liquid adj crystal adj display) and (color\$6 adj scale\$6 and driv\$6 adj circuit\$6) and generat\$6 and digital adj signal and analog adj signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:51
S10 6	14	display and (color\$6 adj scale\$6 and driv\$6 adj circuit\$6) and generat\$6 and digital adj signal and analog adj signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:52
S10 7	17	display and (color\$6 adj scale\$6 and driv\$6 adj circuit\$6) and generat\$6 same digital same signal same analog	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:53
S10 8	2	display and (color\$6 adj scale\$6 and driv\$6 adj circuit\$6) same generat\$6 same digital same signal same analog	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:53
S10 9	2	display and (color\$6 adj scale\$6) same (driv\$6 adj circuit\$6) and generat\$6 same digital same signal same analog	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:53
S11 0	165	display and (color\$6 same scale\$6) same (driv\$6 same circuit\$6) and generat\$6 same digital same signal same analog	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 16:31
S11 1	55	display and (color\$6 same scale\$6) same (driv\$6 same circuit\$6) same generat\$6 same digital same signal same analog	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 14:55
S11 2	29	display and (color\$6 same scale\$6) same (driv\$6 same circuit\$6) same generat\$6 same digital same signal same analog and "345"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 16:31
S11 3	3	"20030001810" and digital	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 16:07
S11 4	32	display and (low same color\$6 same scale\$6) same (driv\$6 same circuit\$6) and generat\$6 same digital same signal same analog	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 16:31

EAST Search History

S11 5	35	display and (low\$6 same color\$6 same scale\$6) same (driv\$6 same circuit\$6) and generat\$6 same digital same signal same analog	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 16:31
S11 6	14	display and (low\$6 same color\$6 same scale\$6) same (driv\$6 same circuit\$6) same generat\$6 same digital same signal same analog and "345"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 16:32
S11 7	21	display same low\$6 same color\$6 same scale\$6 and driv\$6 same circuit\$6 and "345"/87.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 16:40
S11 8	2	display same (low\$6 adj color\$6 adj scale\$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 16:41
S11 9	80	display same (low\$6 same (color\$6 adj scale\$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 16:42
S12 0	26	display with (low\$6 same (color\$6 adj scale\$6))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 16:42
S12 1	0	"20020126106" and color\$6 adj scale\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 16:56
S12 2	1	"20020126106" and gray\$6 adj scale\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/10 16:56
S12 3	7	display and low\$6 same color\$6 same scale\$6 same driv\$6 same circuit\$6 and "345"/87.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/11 07:29
S12 4	2	"5,687,058".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/01/24 16:02